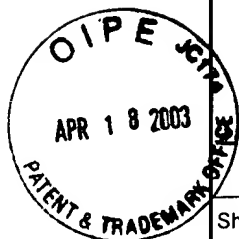


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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	09/929,350
				Filing Date	August 15, 2001
				First Named Inventor	Jerome Eldridge
				Art Unit	1756
				Examiner Name	C. Chu
Sheet	1	of	1	Attorney Docket Number	M4065.0454/P454



U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
C. C.	AA	5,593,926	01/14/1997	Fujihira	
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FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
C. C.	CA	"The New Low-K Candidate: It's a Gas," <i>Semiconductor International</i> (March 1999), p. 38	✓
C. C.	CB	"Thermal Conduction Module: A High-Performance Multilayer Ceramic Package," A.J. Blodgett and D.R. Barbour, <i>IBM Journal of Research and Development</i> , Vol. 26, No. 1(Jan 1982), pp. 30-36	
C. C.	CC	"Future Packaging Trends: CSP vs. Flip Chip," E. Jan Vardaman, 11 th European Microelectronics Conference - 1997, pp. 299	

Examiner Signature	<i>[Signature]</i>	Date Considered	2/3/04
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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